ABSTRACT OF THE DISCLOSURE

A power voltage control circuit for a CPU. The CPU has at least one power indication pin to output a power indication signal. In the control circuit, a register stores an adjustment signal, and combination unit adds the power indication signal and the adjustment signal to output a first signal. A selection unit receives the power indication signal and the adjustment signal. A selection controller controls the selection unit to output the power indication signal or the adjustment signal. A PWM controller supplies a power voltage to the CPU according to the power indication signal or the adjustment signal from the selection unit.